

FIG. 1A

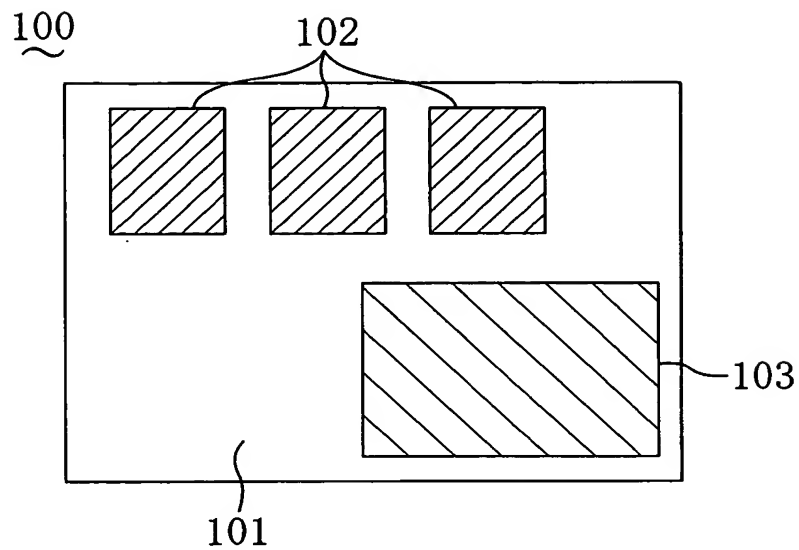


FIG. 1B

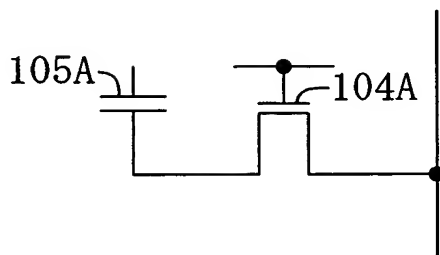


FIG. 1C

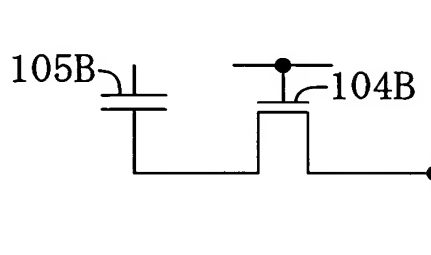


FIG. 1D

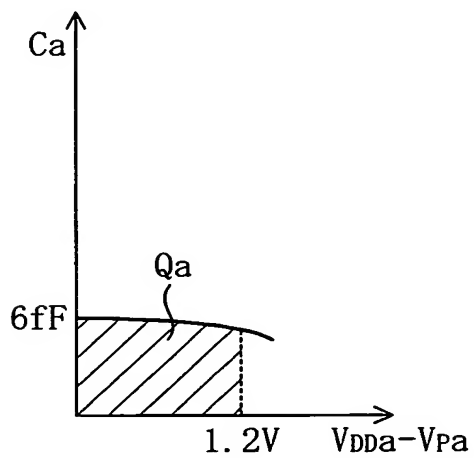


FIG. 1E

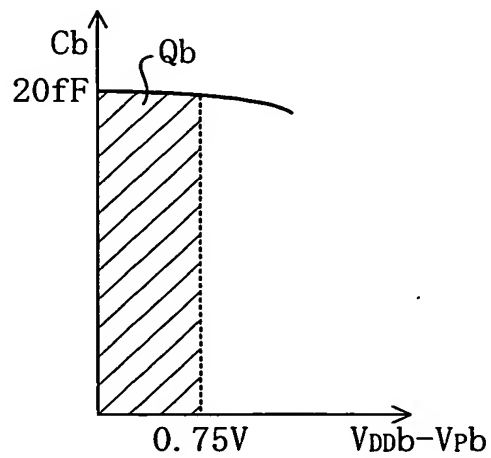
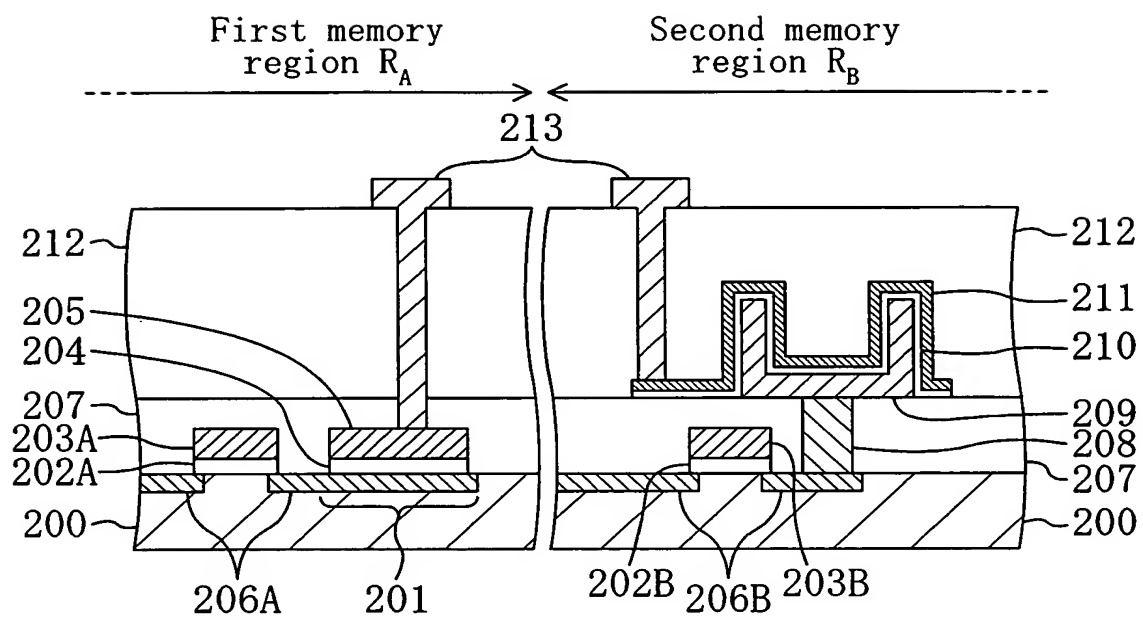


FIG. 2



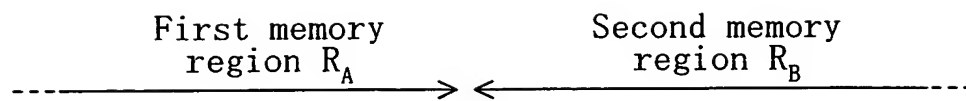


FIG. 3A

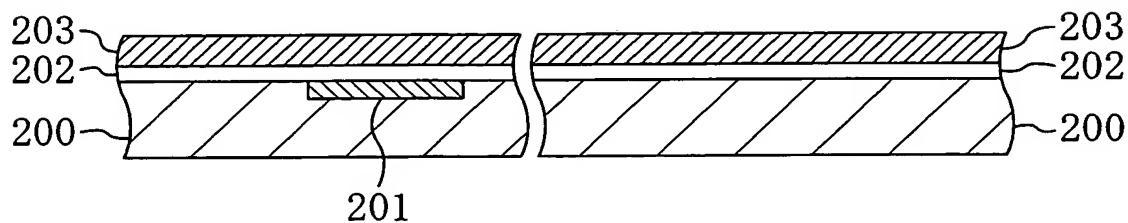


FIG. 3B

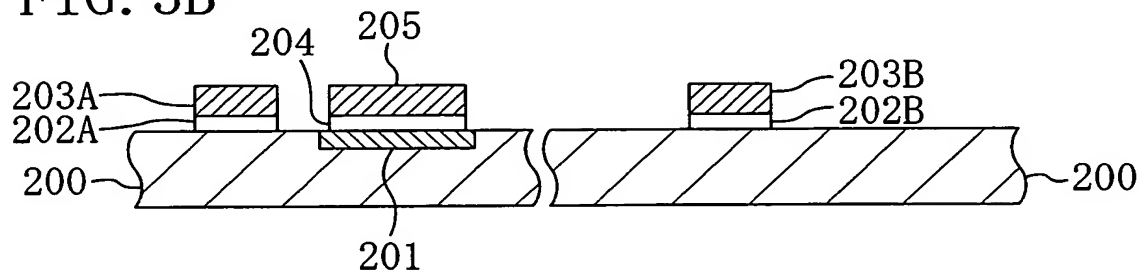


FIG. 3C

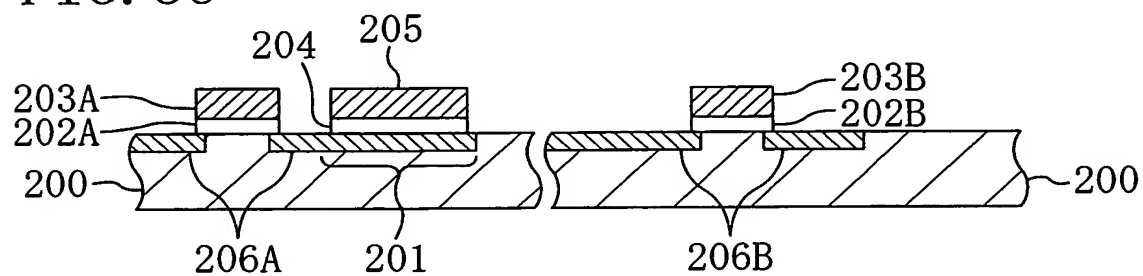
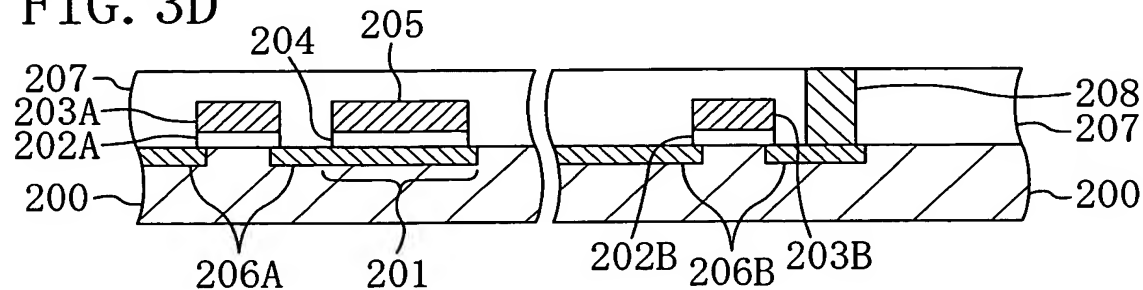


FIG. 3D



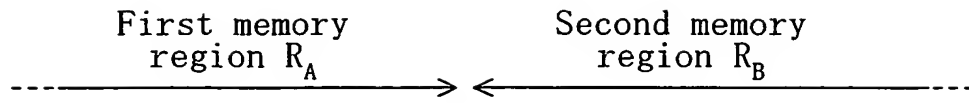


FIG. 4A

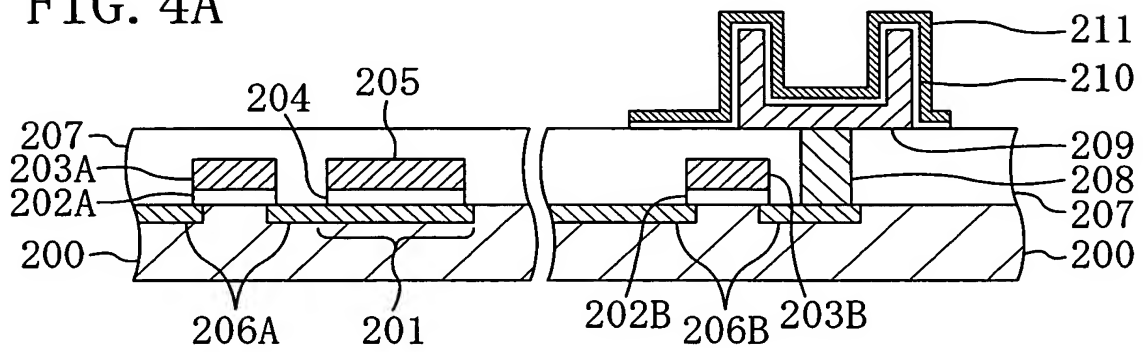


FIG. 4B

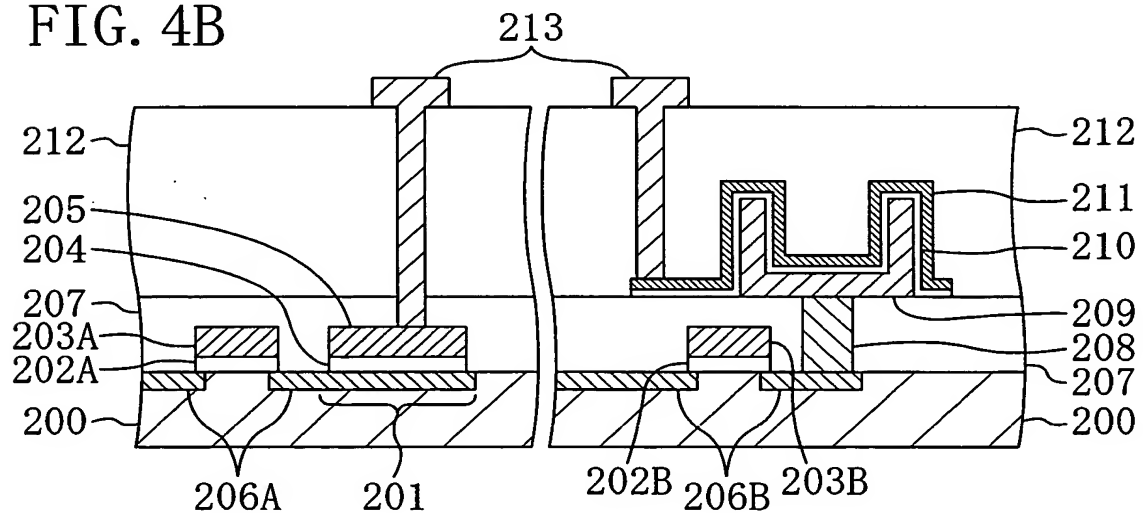
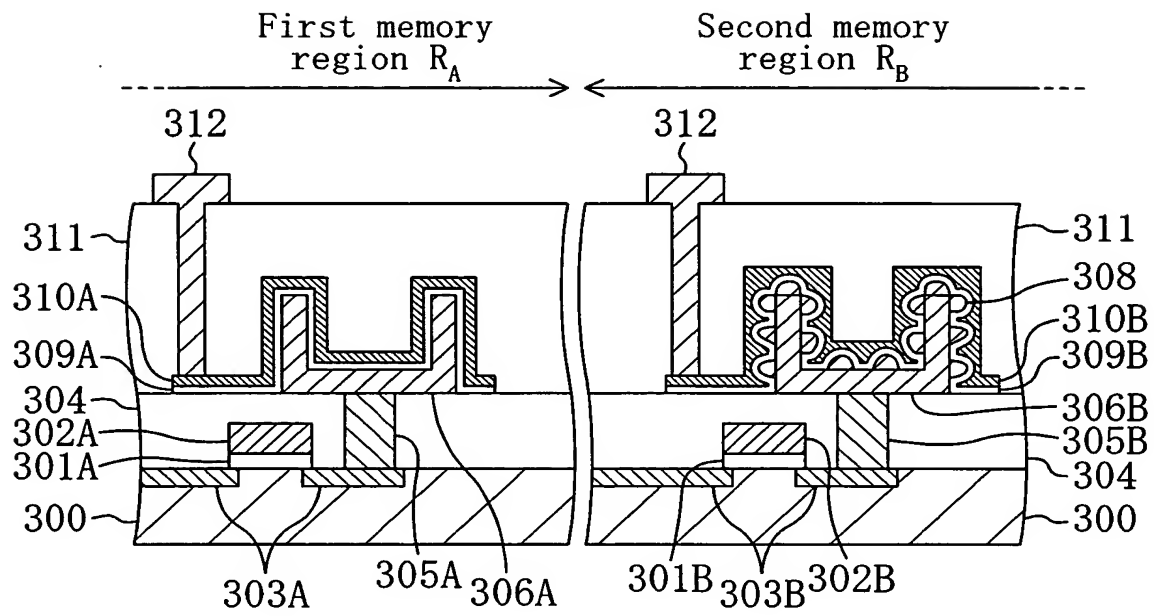


FIG. 5



First memory region R_A
Second memory region R_B

FIG. 6A

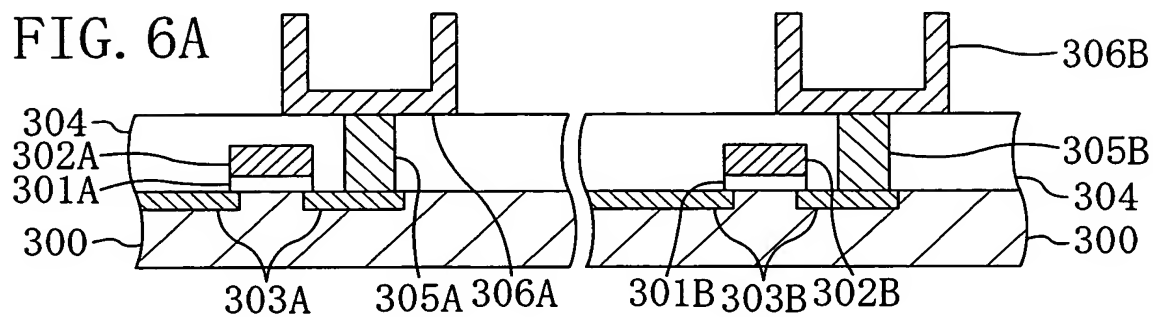


FIG. 6B

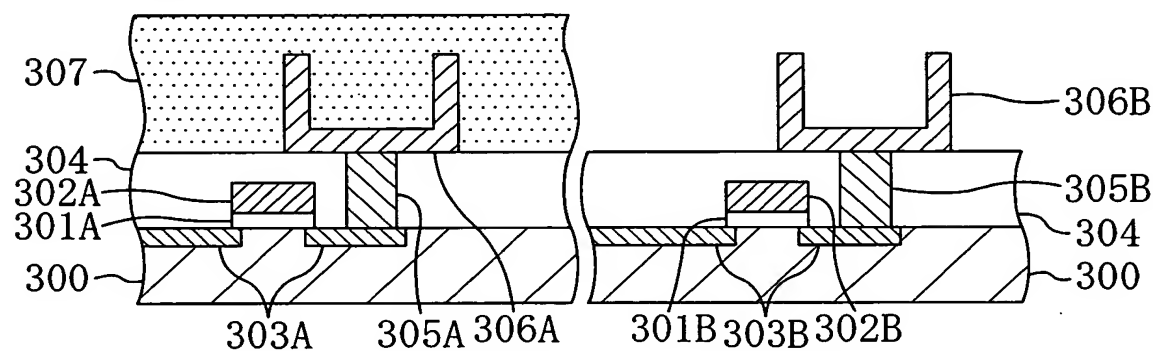


FIG. 6C

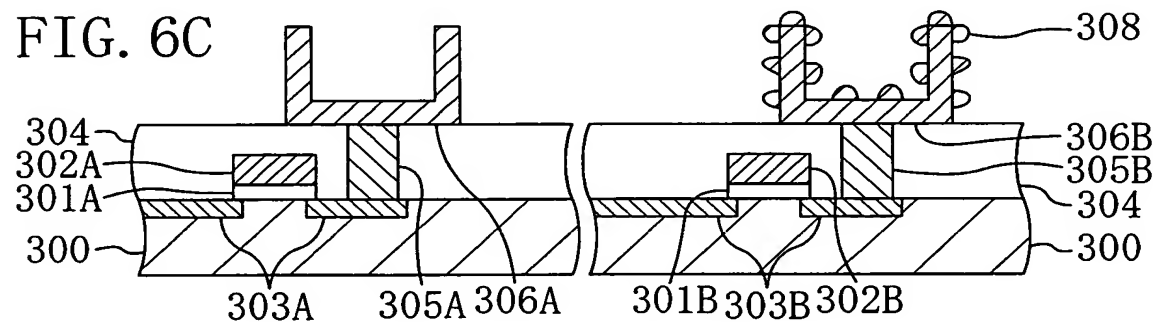


FIG. 6D

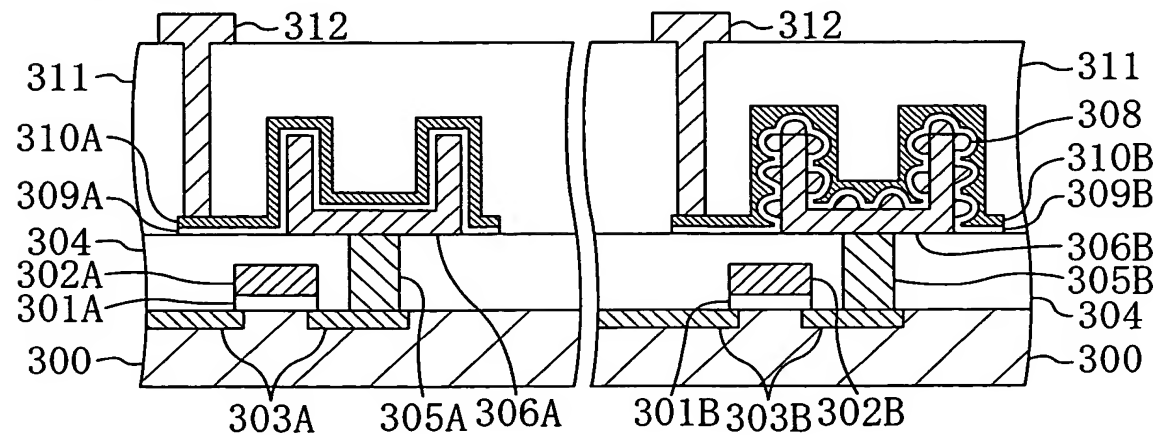
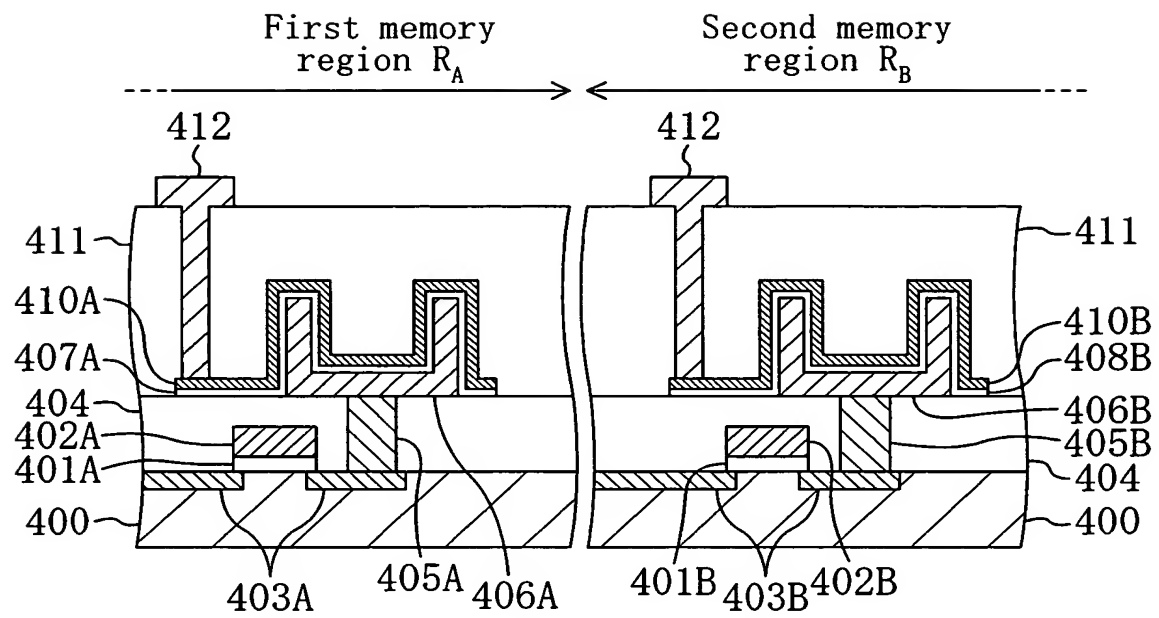


FIG. 7



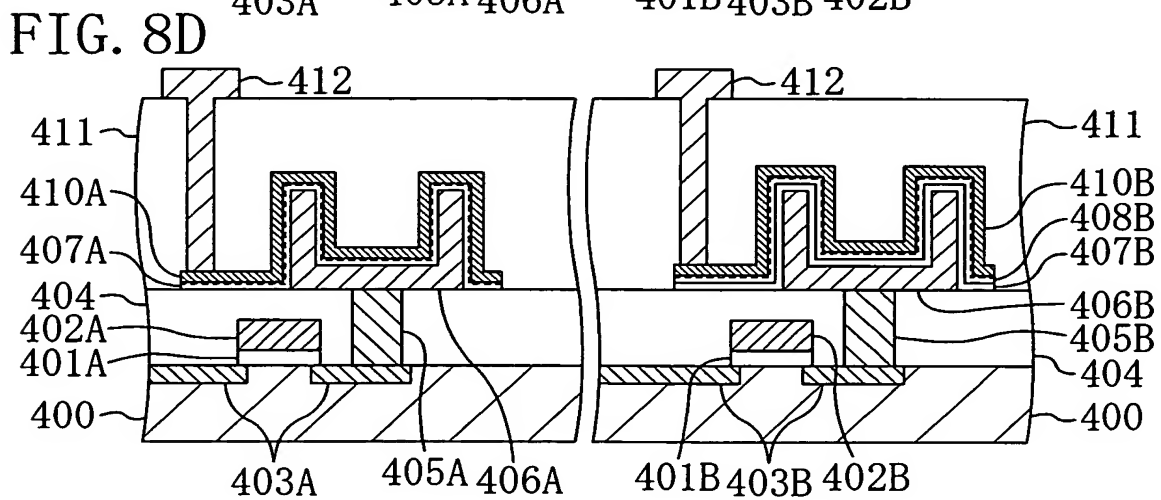
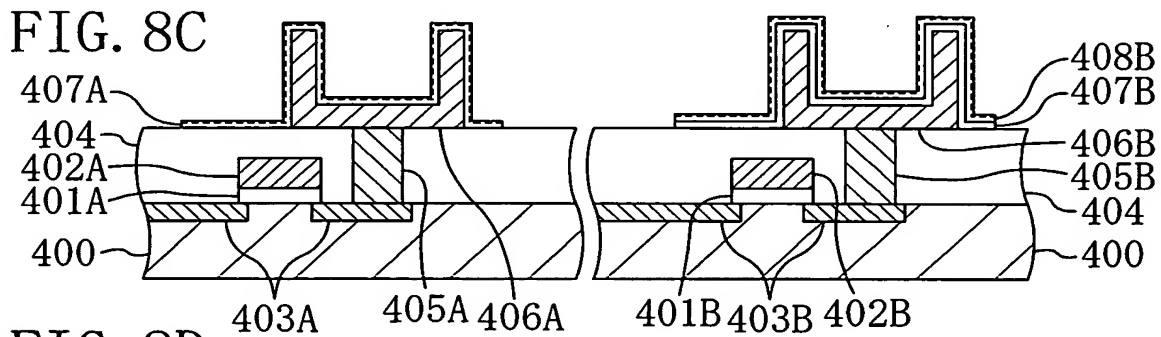
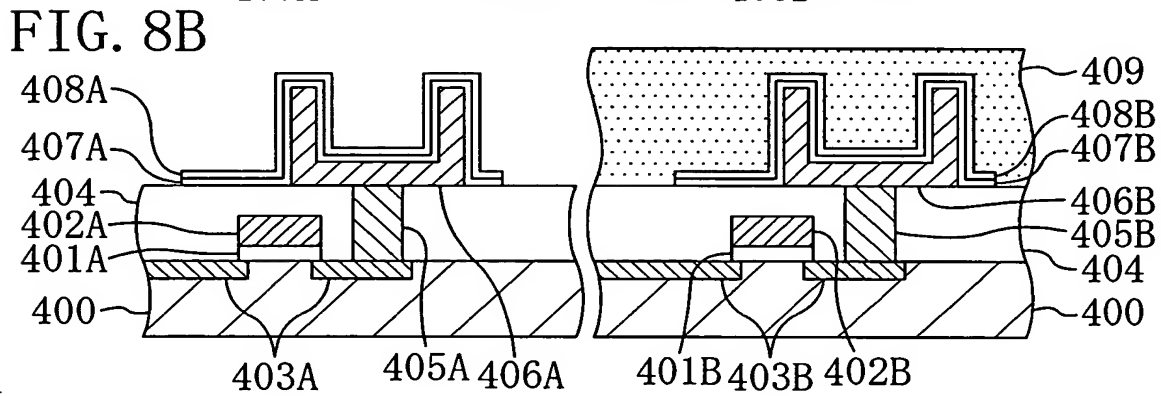
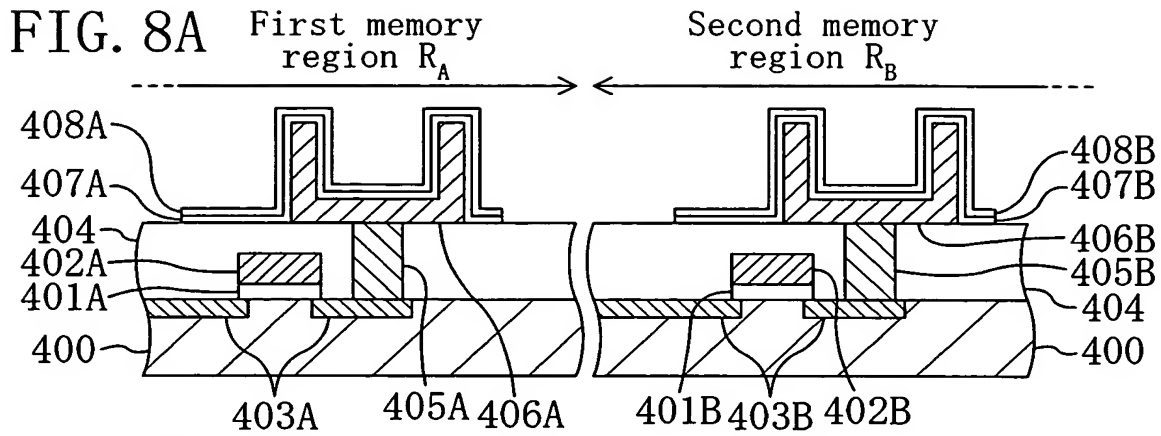
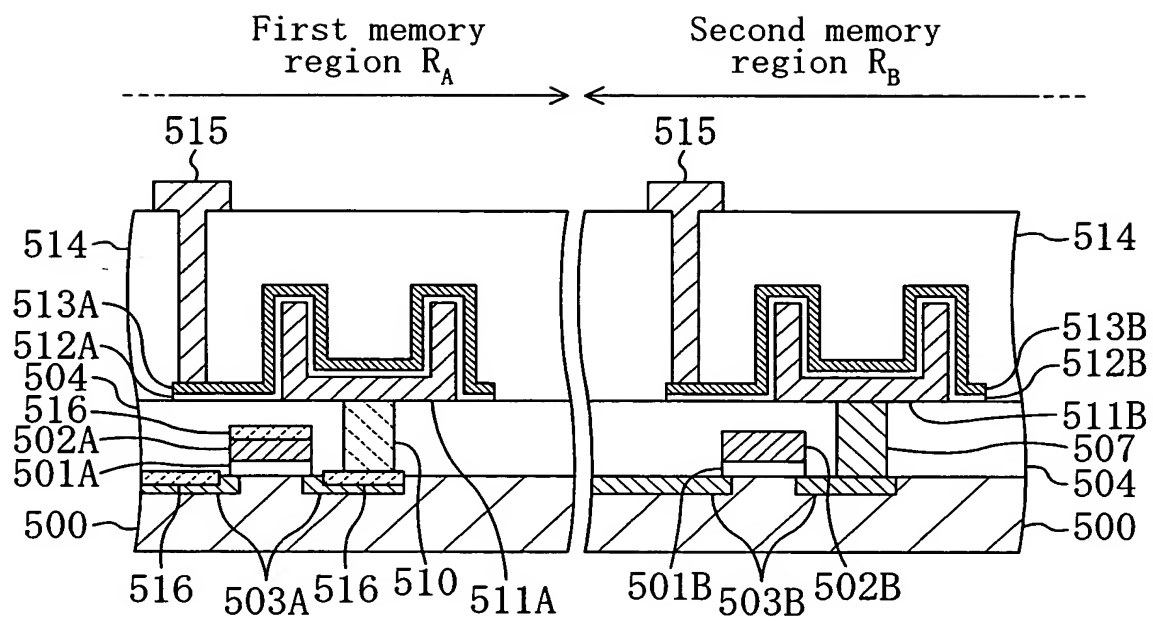


FIG. 9



First memory
region R_A Second memory
region R_B

FIG. 10A

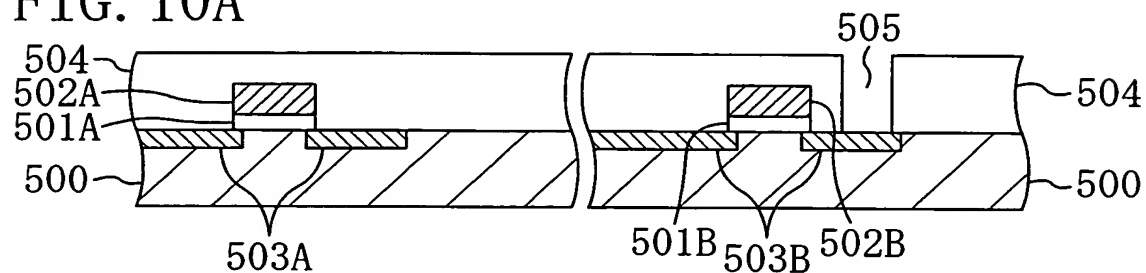


FIG. 10B

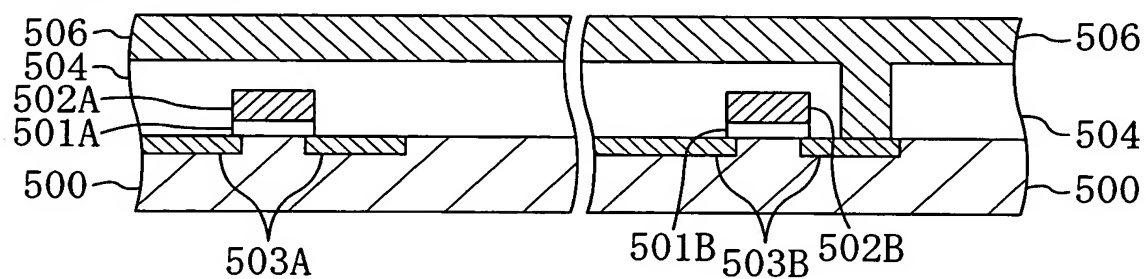


FIG. 10C

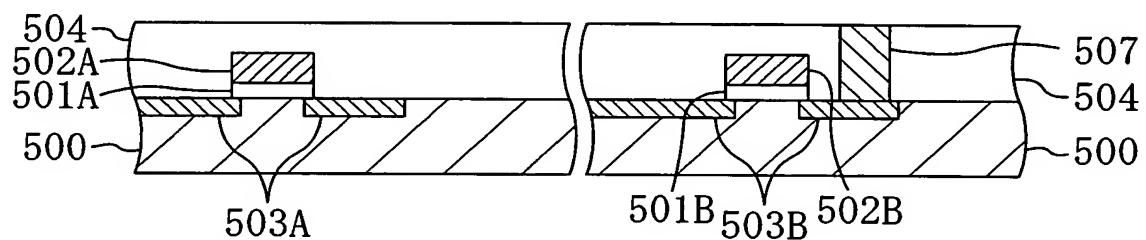


FIG. 10D

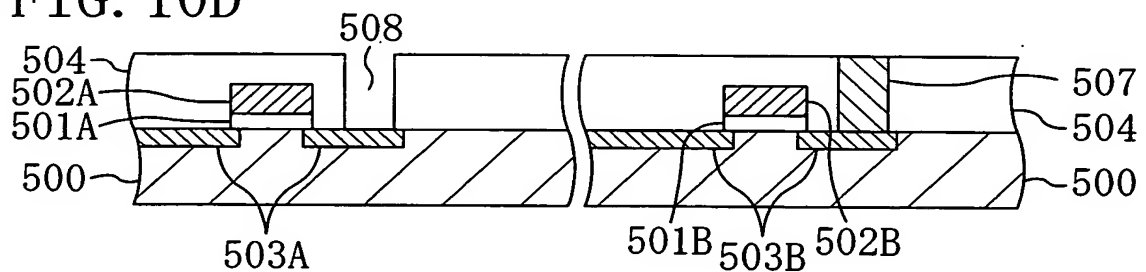
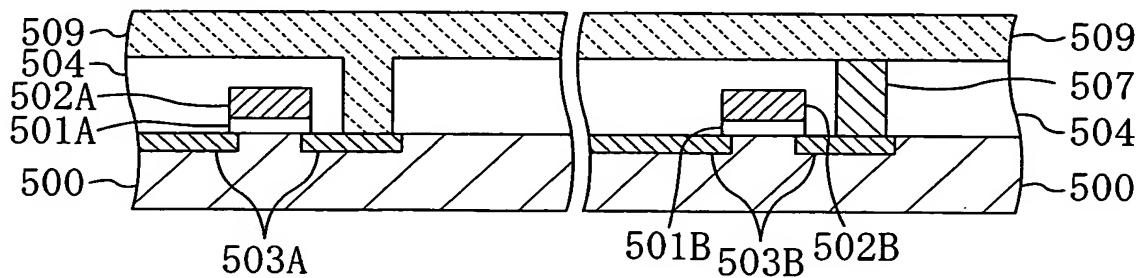


FIG. 10E



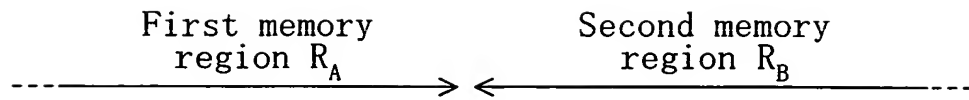


FIG. 11A

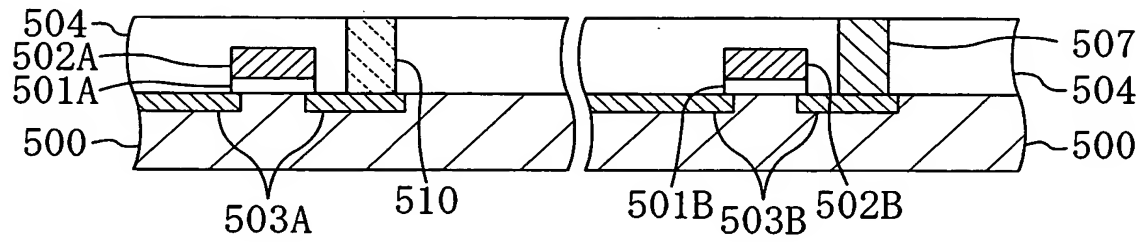


FIG. 11B

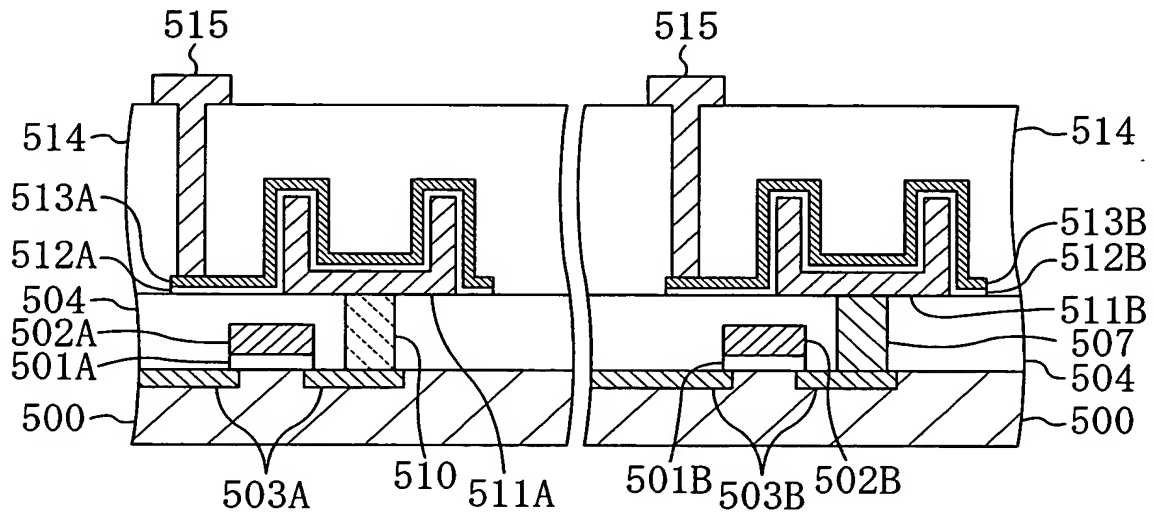


FIG. 12A
PRIOR ART

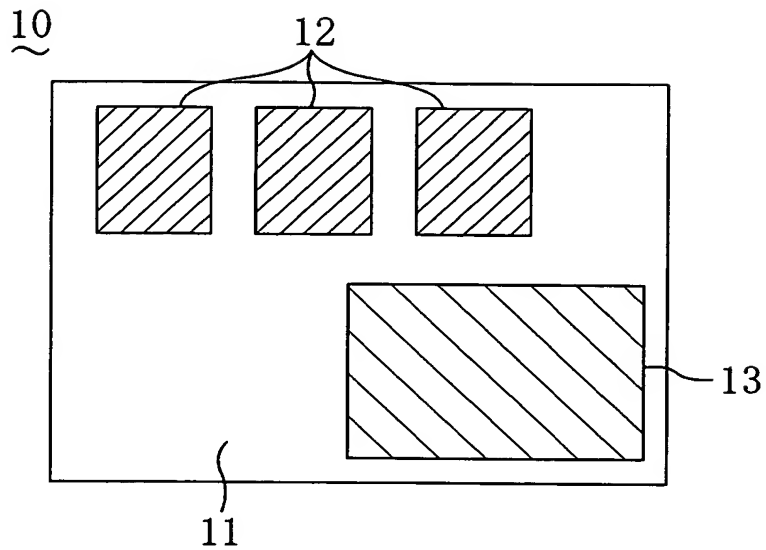


FIG. 12B
PRIOR ART

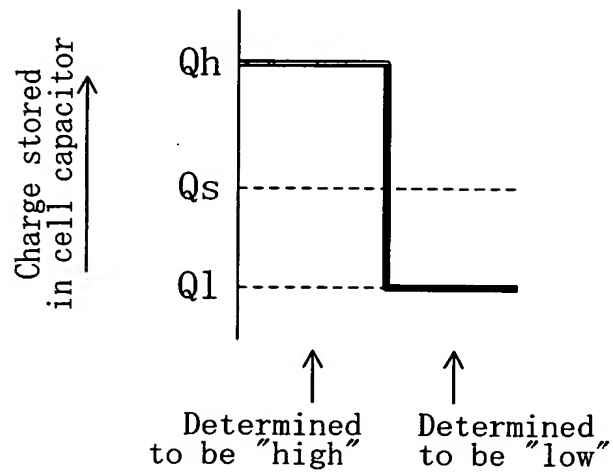


FIG. 12C
PRIOR ART

